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DEC 07 2004**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Lasserre

Serial No.: 09/932,381

Filed: August 17, 2001

For: Local Memory With Indicator Bits to Support Concurrent DMA and
CPU Access

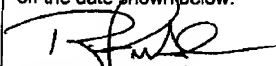
Docket Number: TI-31354

Art Unit: 2188

Examiner: Bragdon

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Rodney M. Anderson

December 7, 2004
Date

FACSIMILE COVER SHEET☒ **FACSIMILE COVER SHEET**☐ NEW APPLICATION☐ DECLARATION☐ ASSIGNMENT☐ FORMAL DRAWINGS☐ INFORMAL DRAWINGS☐ CONTINUATION APP'N☐ DIVISIONAL APP'N☒ **AMENDMENT UNDER RULE 116**
(11 Pages)☒ **FEE TRANSMITTAL (1 Page)**☐ NOTICE OF APPEAL☐ APPEAL☐ ISSUE FEE

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Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75074

PTO/SB/17 (11-04)

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FEE TRANSMITTAL
For FY 2005☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$) 0

Complete if Known

Application Number	09/932,381
Filing Date	August 17, 2001
First Named Inventor	Lasserre
Examiner Name	Bragdon
Art Unit	2188
Attorney Docket No.	TI-31354

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FEE CALCULATION**1. BASIC FILING FEE**

Fee Description	Fee (\$)	Small Entity Fee (\$)	Fee Paid (\$)
Utility Filing Fee	790	395	
Design Filing Fee	350	175	
Plant Filing Fee	550	275	
Reissue Filing Fee	790	395	
Provisional Filing Fee	160	80	

Subtotal (1) \$ 0

FEE CALCULATION (continued)**2. EXTRA CLAIM FEES**

Fee Description	Fee (\$)	Small Entity Fee (\$)
Each claim over 20	18	9
Each independent claim over 3	88	44
Multiple dependent claims	300	150
For Reissues, each claim over 20 and more than in the original patent	18	9
For Reissues, each independent claim more than in the original patent	88	44

Total Claims	Extra Claims	Fee (\$)	Fee Paid (\$)
17	- 20 or HP = 0	18	0

HP = highest number of total claims paid for, if greater than 20

Indep. Claims	Extra Claims	Fee (\$)	Fee Paid (\$)
5	- 3 or HP = 0	88	0

HP = highest number of independent claims paid for, if greater than 3

Multiple Dependent Claims	Fee (\$)	Fee Paid (\$)


Subtotal (2) \$ 0

3. OTHER FEES

Fee Description	Fee (\$)	Small Entity Fee (\$)	Fee Paid (\$)
1-month extension of time	110	55	
2-month extension of time	430	215	
3-month extension of time	980	490	
4-month extension of time	1,530	765	
5-month extension of time	2,080	1,040	
Information disclosure stmt. fee	180	180	
37 CFR 1.17(q) processing fee	50	50	
Non-English specification	130	130	
Notice of Appeal	340	170	
Filing a brief in support of appeal	340	170	
Request for oral hearing	300	150	
Other:			

Subtotal (3) \$ 0

SUBMITTED BY

Signature		Registration No.	31,939	Telephone	972.664.9554
Name (Print/Type)	Rodney M. Anderson	(Attorney/Agent)		Date	7 Dec 2004

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RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2188

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of:

Serge Lasserre (TI-31354)

Conf. No.: 4434

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Art Unit: 2188

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

AMENDMENT UNDER RULE 116**Introductory Comments:**

This paper is presented in response to the Office Action mailed on October 7, 2004. Entry of this amendment in this application, and reconsideration of the final rejection of claims in this application, are respectfully requested.

Proposed amendment to the claims begin on page 2.

Applicant's remarks begin on page 8.